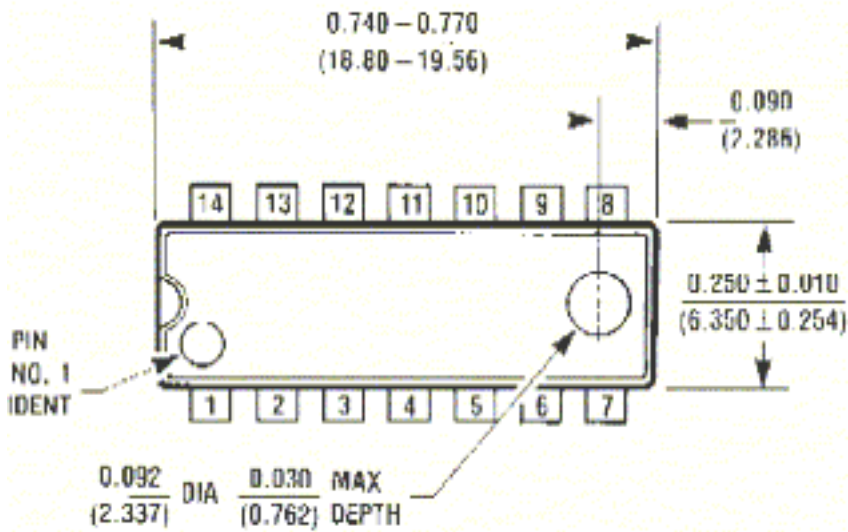


TTL and CMOS logic 74 Series !



Datasheets ii

[CLICK HERE](#) for the TTL and CMOS logic listings!

[CLICK HERE](#) for links to other useful datasheet sites!

This page contains links to datasheets for all the IC's used in my projects, click the part number to read the datasheet. Some of these I scanned myself, others are located on others' pages or the manufacturer's website. If there are any broken links tell me. To read these datasheets you need to [install Adobe Acrobat](#) if you haven't already done so. The scanned datasheets are sometimes easiest to read if you select "Actual Size" in Adobe Acrobat's "View" menu.



A number of these parts are obsolete, and several more will probably become obsolete very soon. I have saved local copies of all of the datasheets linked here, so that they will be preserved if the manufacturers decide to remove the datasheets from their websites. In this case I will replace the URL with a local link. If you find any broken links, let me know.

Many IC's are made by multiple manufacturers, and there have been many rounds of mergers, acquisitions and consolidations in the semiconductor industry. Therefore the manufacturers listed here are not necessarily the ones whose IC's I used, but in all cases chips from a different manufacturer should be interchangeable. In general 74nn chips and 74LSnn chips can also be interchanged, I simply used what was available to me at the time.

Part	Description	Manufacturer	Size	Notes
ii	ii	ii	ii	ii

74100	Dual 4-bit Latch	Texas Instruments	83K		Thanks Walter PE1ABR! [See Note 1]
74284	4-bit binary Multiplier, low byte	Texas Instruments	231K	ii	
74285	4-bit binary Multiplier, high byte	Texas Instruments	231K	ii	
4011	Quad 2-input NAND gate	Fairchild Semiconductor	105K	ii	
4013	Dual D-type Flip-Flop	Fairchild Semiconductor	75K	ii	
4017	Decoded output Decade counter	Fairchild Semiconductor	78K	ii	
4020	14-bit binary Ripple Counter	Fairchild Semiconductor	83K	ii	
4022	Divide-by-8 counter/divider	Fairchild Semiconductor	78K	ii	
4026	Decade counter with 7-segment output	ST Microelectronics	84K	ii	
74HC4040	12-bit binary Ripple Counter	Fairchild Semiconductor	99K	ii	
4040	12-bit binary Ripple Counter	Fairchild Semiconductor	83K	ii	
74HC4060	14-bit binary Ripple Counter	Fairchild Semiconductor	85K	ii	
4060	14-bit binary Ripple Counter	Fairchild Semiconductor	83K	ii	
4068	8-input NAND/AND gate	Texas Instruments	182K	ii	
4069	Quad Unbuffered inverter	Fairchild Semiconductor	79K	ii	
4081	Quad 2-input AND gate	Fairchild Semiconductor	97K	ii	
4511	BCD to 7-Segment Decoder/Driver	Fairchild Semiconductor	66K	ii	
4518	Dual 4-bit binary counter	Texas Instruments	248K	ii	
4538	Dual monostable multivibrator	Fairchild Semiconductor	138K	ii	
4464	64K x 4 Dynamic RAM	ii	1K		Pinout only [Note 2]
6264	8K x 8 Static RAM	Hitachi Semiconductor	83K	ii	
44256	256K x 4 Dynamic RAM	OKI Semiconductor	244K	ii	
62256	32K x 8 Static RAM	Hitachi Semiconductor	143K	ii	
431000	128K x 8 Static RAM	NEC	194K	ii	

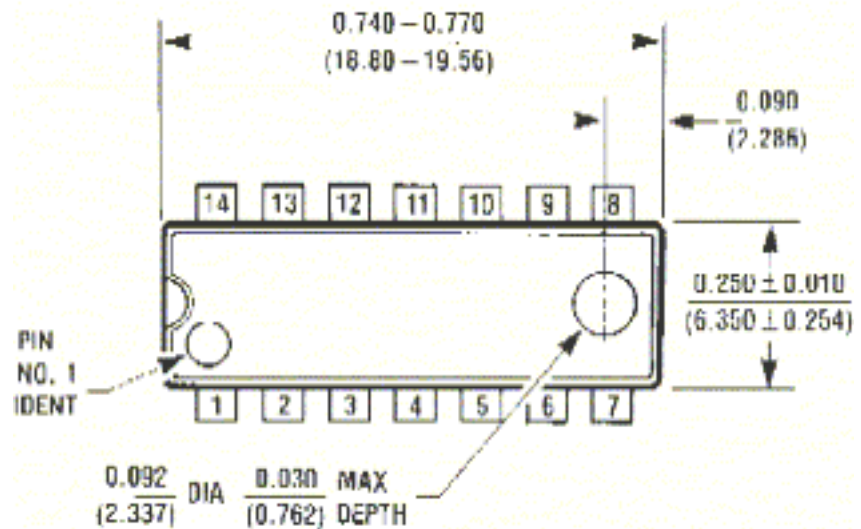
28C64	8K x 8 CMOS EEPROM	Microchip Technology Inc	101K	ii
74C917	6-Digit Hex Display Controller	National Semiconductor	475K	Scan of my paper copy
PCM54	16-bit Digital to Analog converter	Burr-Brown	64K	ii
UM70C171	Colour Palette with 3 x 6-bit Video DAC	United Microelectronics	1,330K	Scan of my paper copy
Z80	Z80 8-bit microprocessor	Zilog	1,512K	Scan [Note 3]
Z8400	Z80 8-bit microprocessor equivalent	SGS (now ST Microelectronics)	2,407K	Z8400 = Z80 equivalent
MK3880	Z80 8-bit microprocessor equivalent	Mostek	2,980K	92-page manual!
555	Timer	National Semiconductor	401K	ii
741	Operational Amplifier	National Semiconductor	177K	ii
7660	Switched capacitor voltage converter	National Semiconductor	135K	ii
7805	+5V Voltage Regulator (LM340T-5.0)	National Semiconductor	515K	ii
7812	+12V Voltage Regulator (LM340T-12)	National Semiconductor	515K	ii
7912	-12V Voltage Regulator (LM320L...)	National Semiconductor	151K	ii
CA3140	Operational Amplifier	Intersil Corp	1.01M	ii
CA3240	Dual Operational Amplifier	Intersil Corp	399K	ii
L4960	2.5A Power Switching Regulator	ST Microelectronics	190K	ii
LF398	Sample and Hold Circuit	National Semiconductor	520K	ii
LM317	Variable Voltage Regulator	National Semiconductor	646K	ii
LM324	Quad Operational Amplifier	National Semiconductor	466K	ii
LM759	Power Operational Amplifier	National Semiconductor	207K	ii
MM5387	Alarm Clock	National Semiconductor	511K	More: Pinout and notes
MAX7219	8-digit LED Display Drivers	MAXIM	187K	ii
MAX7221	8-digit LED Display Drivers	MAXIM	187K	ii
ii	ii	ii	ii	ii

[**Note 1**] I am unable to locate a datasheet for the [4464](#) (64K x 4-bit dynamic RAM). It can be assumed similar regarding specifications and timing to the [44256](#).

[**Note 2**] This [Z80](#) datasheet was scanned from my old paper copy. This is an original [Zilog](#) datasheet, not one of the licensed Z80 equivalents such as the [Z8400](#) by [ST Microelectronics](#). As far as I know this is the **ONLY** online Zilog Z80 datasheet. The [Z8400](#) datasheet does give more detailed information on various aspects of the Z80.

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Manufacturers



[Hitachi Semiconductor](#)

[Fairchild Semiconductor](#)

[Texas Instruments](#)

[Burr-Brown](#)

[Micropchip Technology Inc](#)

[NEC](#)

[Intersil Corp](#)

[National Semiconductor](#)

[ST Microelectronics](#)

[United Microelectronics \(UMC\)](#)

[OKI Semiconductor](#)

[Zilog](#)

[MAXIM](#)

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TTL and CMOS Logic listings

Where I have located a datasheet, you'll see an [X](#). Click it to view the datasheet. I aim to fill in as many of the blanks as possible over time. If you have any additional information or find non-functional links, then please [Send me an Email](#).

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ii	ii	ii	74	74	74	74	74	74	ii	74	74	74	74
Type	Description	Pins	HC	HCT	AC	ACT	F	4000	ALS	AS	ii	LS	
00	Quad 2 Input NAND Gate	14	X	X	X	X	X	ii	X	X	X	X	
01	Quad 2 Input NAND Gate (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	
02	Quad 2 Input Positive NOR Gate	14	X	ii	X	X	X	ii	X	ii	X	X	
03	Quad 2 Input Positive NAND Gate	14	ii	ii	ii	ii	ii	ii	X	ii	ii	X	
04	Hex Inverter	14	X	X	X	X	X	ii	X	X	X	X	
05	Hex Inverter (OC)	14	ii	X	X	ii	ii	ii	X	ii	X	X	
06	Hex Inverter Buffer/Driver (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	X	X	
07	30V,40mA Hex Buffer Driver (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	X	X	
08	Quad 2 Input Positive AND Gate	14	X	X	X	X	X	ii	X	X	ii	X	
09	Quad 2 Input Positive AND Gate (OC)	14	ii	ii	ii	ii	ii	ii	X	ii	ii	X	

10	Triple 3 Input Positive NAND Gate	14	ii	ii	X	X	X	ii	X	X	X	X
11	Triple 3 Input Positive AND Gate	14	ii	ii	X	ii	X	ii	X	ii	ii	X
12	Triple 3-Input Nand (Open Collector)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
13	Dual 4-Input Schmitt Trig. Nand	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
14	Hex Schmitt Trigger	14	X	X	X	X	X	ii	X	ii	X	X
16	Hex Inverter Buffer/Driver (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
17	15V,40mA Hex Buffer/Driver (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
19	Hex Schmitt-Trigger Inverter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
20	Dual 4 Input Positive NAND Gate	14	ii	ii	X	ii	X	ii	X	ii	ii	X
21	Dual 4 Input Positive AND Gate	14	ii	ii	ii	ii	ii	ii	X	X	ii	X
22	Dual 4-Input Nand Gate (Open Collector)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
23	Expandable Dual 4-Input Nor Gate	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
25	Dual 4 Input Positive NOR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
26	Quad 2 Input NAND Gate (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
27	Triple 3 Input NOR Gate	14	ii	ii	ii	ii	ii	ii	X	X	ii	X
28	Quad 2 Input NOR Buffer	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
30	8 Input Positive NAND Gate	14	ii	ii	ii	ii	ii	ii	X	X	ii	X
31	Delay Element	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
32	Quad 2 Input Positive OR Gate	14	X	X	X	X	X	ii	X	X	X	X
33	Quad 2 Input NOR Buffer (OC)	14	ii	ii	ii	ii	ii	ii	X	ii	ii	X
34	Hex Non-Inverter	14	ii	ii	ii	ii	ii	ii	ii	X	ii	ii
35	Hex Non-Inverter (OC)	14	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
37	Quad 2 Input Positive NAND Buffer	14	ii	ii	ii	ii	ii	ii	X	ii	ii	X
38	Quad 2 Input NAND Buffer (OC)	14	ii	ii	ii	ii	X	ii	X	ii	X	X
40	Dual 4-Input Nand Buffer Gates	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
41	Bcd-Todecimal Decoder/Driver (Nixie)	16	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
42	Excess-3 Decimal Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
43	Excess-3-Gray Decimal Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
44	Excess-3-Gray Decimal Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
45	BCD to Decimal Decoder/Driver (OC)	16	ii	ii	ii	ii	ii	ii	ii	ii	X	ii

46	BCD to 7 Segment Decoder/Driver (30V)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
47	BCD to 7 Segment Decoder/Driver (15V)	16	ii	ii	ii	ii	ii	ii	ii	ii	X	X
48	BCD to 7 Segment Decoder/Driver	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
50	Xpan. Dual 2-Wide 2in A-O-I Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
51	Dual 2 Wide 2 Input AND/OR Inv. Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
53	Xpan. 4-Wide 2in And/Or Inverter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
54	4 Wide 2-Input And/Or Inverter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
59	2 Wide 2-3 In And/Or Inverter Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
60	Dual 4-Input Expanders	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
70	Edge Triggered Jk Flip Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
72	J-K Master Slave Flip Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
73	Dual J-K Master Slave Flip/Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
74	Dual D Type Edge Triggered Flip/Flop	14	X	X	X	X	X	ii	X	X	ii	X
75	Quad Bi-Stable Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
76	Dual J-K Master Slave Flip/Flop	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
77	4 Bit Bi-Stable Latch	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
79	Dual D Flip Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
80	Gated Full Adder	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
82	2-Bit Full Adder	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
83	4 Bit Binary Full Adder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
85	4 Bit Magnitude Comparator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
86	Quad 2 Input XOR Gate	14	X	ii	X	ii	X	ii	X	ii	ii	X
89	64-Bit RAM	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
90	Decade Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
91	8-Bit Shift Register	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
92	Divide-by-12 Decade Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
93	4 Bit Binary Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
94	4-Bit Shift Reg. Par. In/Ser. Out	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
95	4-Bit Right Shift Left Shift Reg.	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
96	5-Bit Par. In Par. Outshift Reg.	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii

97	6 Bit Asynchronous Binary Rate Multiplier	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
100	Dual 4-Bit Bistable Latch	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
104	Gated J-K Master Slave Flip Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
107	Dual J-K Master Slave Flip/Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	X
109	Dual J-K Positive Edge Triggered Flip/Flop	16	ii	ii	X	X	X	ii	X	ii	ii	X	ii
112	Dual J-K Negative Edge Triggered Flip/Flop	16	ii	ii	ii	ii	X	ii	X	ii	ii	X	ii
121	One Shot Multivibrator	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
122	Retriggerable Monostable Multivibrator	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
123	Dual One Shot Multivibrator	16	X	ii	ii	ii	ii	ii	ii	ii	ii	X	X
124	Dual Voltage Controlled Oscillator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
125	Tri-State Quad Buffer	14	X	ii	X	X	X	ii	X	ii	ii	X	ii
126	Tri-State Quad Buffer	14	X	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
128	Quad 2 Input NOR Line Driver	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
132	Quad Schmitt Trigger	14	X	ii	ii	ii	X	ii	ii	ii	ii	ii	X
133	13 Input NAND Gate	16	ii	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
136	Quad Exclusive OR Gate (OC)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
137	3/8 Decoder Multiplexer	16	ii	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
138	Expandable 3/8 Decoder	16	X	X	X	X	X	ii	X	ii	ii	X	ii
139	Dual 2 to 4 Decoder Demultiplexer	16	ii	ii	X	X	X	ii	X	ii	ii	X	ii
140	Dual 4 Input NAND Line Driver	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
141	Bcd-Todecimal Decoder/Driver (Nixie)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
145	BCD to Decimal Decoder/Driver (OC)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	X
147	10 to 4 Line Priority Encoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
148	8 to 3 Priority Encoder	16	ii	ii	ii	ii	X	ii	ii	ii	ii	ii	X
150	16 Line Multiplexer	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	X	ii
151	8 Line Multiplexer	16	X	ii	X	X	X	ii	X	ii	ii	X	ii
152	8-Channel Data Selector	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
153	Dual 4 Input Multiplexer	16	ii	ii	X	X	X	ii	X	ii	ii	X	ii

154	4 Line to 16 Line Decoder Demultiplexer	24	<u>X</u>	ii	ii	ii	ii	ii	ii	ii	<u>X</u>	ii
155	Dual 2 to 4 Demultiplexer	18	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
156	Dual 2 to 4 Demultiplexer (OC)	16	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>
157	Quad 2 Input Multiplexer (9322)	16	<u>X</u>	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
158	Quad 2 Input Data Selector/ Multiplexer	16	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
159	4 to 16 Line Decoder	24	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>	ii
160	Synchronous 4 Bit Decade Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
161	Synchronous 4 Bit Binary Counter	16	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
162	Synchronous 4 Bit Binary Counter	16	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
163	Binary Synchronous 4 Bit Counter	16	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
164	8 Bit Serial In/Parallel Out Shift Register	14	<u>X</u>	<u>X</u>	ii	ii	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
165	8 Bit Parallel In/Serial Out Shift Register	16	<u>X</u>	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>
166	8 Bit Parallel In/Serial Out Shift Register	16	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>
169	Synchronous 4 Bit Binary Up/Down Counter	16	ii	ii	<u>X</u>	ii	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
170	4 X 4 Register File (O/C)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
173	Tri-State Quad D Flip/Flop	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
174	Hex D Type Flip/Flop with Clear	16	<u>X</u>	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
175	Quad D Type Edge Triggered Flip/ Flop	16	<u>X</u>	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
176	Presettable 35-Mhz Decade Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
177	Presettable 35-Mhz 4-Bit Binary Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
180	8-Bit Odd/Even Parity Generator/ Checker	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
181	Airthmetic Logic Unit / Function Generator	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
182	Look-Ahead Carry Generator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
189	64 Bit BIPOLAR Scratch Pad Memory	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii

190	Synchronous Decade Up/Down Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
191	Synchronous Binary Up/Down Counter	16	ii	ii	<u>X</u>	ii	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
192	Synchronous Bcd Decade Up/Down Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
193	Synchronous Binary Up/Down Counter	16	ii	ii	ii	ii	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
194	4 Bit Bi-Directional Universal Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
195	4 Bit Parallel Access Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
196	Presettable 50-Mhz 4-Bit Decade Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
197	Presettable 50-Mhz 4-Bit Binary Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
199	8-Bit Bidirectional Universal Shift Register	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
221	Dual Monostable Multivibrator with Clear	16	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>	<u>X</u>
224	16 x 4 Synchronous FIFO with 3-state outputs	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
229	16 x 5 synchronous FIFO memory	20	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
232	16 x 4 synchronous FIFO memory	16	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
236	64 x 4 synchronous FIFO memory	16	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
237	3 to 8 Line Latched Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
238	3 to 8 Line Non-Inverting Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
240	Octal Tri-State Inverter Buffer	20	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
241	Octal Tri-State Buffer	20	ii	ii	<u>X</u>	<u>X</u>	ii	ii	<u>X</u>	ii	ii	<u>X</u>
243	Quad Bus Transceiver	14	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>
244	Octal Tri-State Buffer	20	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
245	Octal Tri-State Transceiver	20	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
247	BCD to 7-segment Decoder/Driver	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
251	Data Selector/Multiplexer	16	<u>X</u>	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
253	Tri-State Dual 4 Input Multiplexer	16	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	ii	ii	<u>X</u>
257	Tri-State Quad 2 Input Multiplexer	16	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>

258	Tri-State Quad 2 Input Multiplexer	16	ii	ii	ii	X	ii	ii	X	X	ii	X
259	8 Bit Addressable Latch	16	X	ii	ii	ii	ii	ii	X	ii	ii	X
260	Dual 5 Bit Input NOR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
265	Quad Complementary-Output Elements	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
266	Quad 2-Input exclusive-NOR Gate, O/C outputs	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
269	8 Bit Up/Down Counter	24	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
273	Octal D Type Flip/Flop with Clear	20	X	X	X	X	X	ii	X	ii	ii	X
276	Quad J-K Flip Flop	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
279	Quad R-S Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
280	9 Bit Parity Generator/Checker	14	ii	ii	X	ii	X	ii	X	X	ii	X
283	4 Bit Binary Adder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
284	Tri-State 4-Bit Multiplexer (Multiplier?)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
285	Tri-State 4-Bit Multiplexer (Multiplier?)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
286	9 Bit Parity Generator/Checker	14	ii	ii	ii	ii	ii	ii	ii	X	ii	ii
292	16 Bit Programmable Frequency Divider	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
293	4-bit Binary Counters	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
294	16 Bit Programmable Frequency Divider	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
297	Digital Phase Locked Loop	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
298	Quad 2-input Multiplexers with storage	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
299	8 Bit Shift/Storage Register	20	ii	ii	X	X	ii	ii	X	ii	ii	X
321	Crystal Oscillator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
323	8 Bit Shift/Storage Register	20	ii	ii	ii	X	ii	ii	X	ii	ii	ii
348	8 to 3 Priority Encoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
351	Dual Data Selector/Multiplexer T.S. Output	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
365	Tri-State Hex Buffer	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
366	Hex Inverting Bus Drivers	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
367	Tri-State Hex Buffer	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X

368	Tri-State Hex Inverter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
373	Tri-State Octal Transparent Latch	20	X	X	X	X	X	ii	X	X	ii	X
374	Tri-State Octal D Flip/Flop	20	X	X	X	X	X	ii	X	X	ii	X
375	Quad Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
377	Octal D Type Flip/Flop with Enable	20	ii	ii	X	X	X	ii	ii	ii	ii	X
378	Hex D Flip/Flop with Enable	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
379	Hex D Flip/Flop with Enable, Inverted Outputs	16	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
390	Dual Decade Ripple Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
393	Dual 4 Bit Binary Ripple Counter	14	X	ii	ii	ii	ii	ii	ii	ii	ii	X
399	Quad 2 Input Multiplexer	16	ii	ii	X	X	X	ii	ii	ii	ii	X
423	Multivibrator (no trigger from clear)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
442	Quad tridirectional bus drivers	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
465	Octal Buffer Tri-State Output	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
518	Octal Comparator	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
520	Octal Comparator	20	ii	ii	X	X	X	ii	X	ii	ii	ii
521	Octal Comparator	20	ii	ii	X	X	X	ii	X	ii	ii	ii
533	Octal Transparent latch, Tri-State Output	20	ii	ii	ii	X	X	ii	X	ii	ii	ii
534	Inverting Octal D Flip/Flop	20	ii	ii	ii	X	X	ii	X	ii	ii	ii
540	Octal Buffer and Line DR/RX Inverted	20	X	X	X	ii	X	ii	X	ii	ii	X
541	Octal Buffer and Line DR/RX Non- Inverting	20	X	X	X	X	X	ii	X	ii	ii	X
543	Octal Registered Transceiver	24	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
545	Octal Tri-State Transceiver	24	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
561	Synchronous 4-bit counters, 3-state	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
563	Inverted Octal D Type Latch	20	ii	ii	ii	X	ii	ii	X	ii	ii	ii
564	Octal D Type Latch	20	ii	ii	ii	ii	X	ii	X	ii	ii	ii
569	Synchronous Bi-Directional Binary Counter	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
573	Octal D Type Latches	20	X	X	X	X	X	ii	X	X	ii	ii
574	Octal D Type Flip/Flop	20	X	X	X	X	X	ii	X	X	ii	ii

575	Octal D Type Flip/Flop	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
576	Inverting Octal D Type Flip/Flop	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
577	Octal D Type Flip/Flop	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
579	8 Bit Up/Down Counter	20	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
580	Inverting Octal D Type Latch	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
589	8 Bit Shift Register with Input Latch	16	X	ii	ii	ii	ii	ii	ii	ii	ii	ii
590	8 Bit Binary Counter, O/P Register 3-state	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
592	8 Bit Binary Counter with Input Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
593	8 Bit Binary Counter, O/P Register TS I/O	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
594	8 Bit Binary Counter, O/P Register TS I/O	16	X	ii	ii	ii	ii	ii	ii	ii	ii	X
595	8 Bit Shift Register with Output Latch	16	X	ii	ii	ii	ii	ii	ii	ii	ii	X
596	8 Bit Shift Register with Output Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
597	8 Bit Shift Register with Input Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
598	8 Bit Shift Register with Input Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
604	16 to 8 Multiplexer (High Speed)	28	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
620	Octal Bus Transceivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X
621	Octal Bus Transceivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X
623	Octal Bus Transceivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X
624	Voltage Controlled Oscillator	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
628	Voltage Controlled Oscillator	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
629	Voltage Controlled Oscillator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	X
638	Octal Bus Transceivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
639	Octal Bus Transceivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
640	Octal Bus Transceiver	20	ii	ii	ii	ii	ii	ii	X	X	ii	X
641	Octal Bus Transceiver	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X
642	Octal Bus Transceiver	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X
645	Octal Bus Transceiver	20	ii	ii	ii	ii	ii	ii	X	ii	ii	X

646	Octal TS Bus Transceiver Non-Inverting	24	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
648	Octal TS Bus Transceiver Inverting	24	ii	ii	<u>X</u>	ii	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
651	Octal Bus Transceiver Non-Inverting	24	ii	ii	ii	ii	ii	ii	<u>X</u>	<u>X</u>	ii	ii
652	Octal Bus Transceiver Non-Inverting	24	ii	ii	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>	<u>X</u>	ii	<u>X</u>
653	Octal Bus Transceiver	24	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
654	Octal Bus Transceiver	24	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
666	Octal D Type Latch, 3-state	24	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
667	Octal D Type Latch, 3-state	24	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
669	Synchronous 4-Bit Up/Down Binary Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
670	Tri-State 4 4 Register File	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
673	16 Bit Shift Register,16 Bit Parallel Output	24	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	<u>X</u>
674	16 Bit Shift Register,16 Bit Parallel Output	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
675	16 Bit Shift Register,16 Bit Parallel Output	24	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
676	16 Bit Shift Register,16 Bit Parallel Output	24	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
679	12-Bit Address Comparators	20	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
682	8 Bit Magnitude Comparator	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
684	8 Bit Magnitude Comparator	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
688	8 Bit Magnitude Comparator	20	<u>X</u>	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	<u>X</u>
697	Synchronous 4-Bit Up/Down Binary Counter	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	<u>X</u>
760	Octal Buffer & Line Driver, OC	20	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
779	8-bit bidirectional binary counter, 3-state	16	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
804	Hex 2-input NAND drivers	20	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
805	Hex 2-input NOR drivers	20	ii	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii
821	10 Bit D Type Flip/Flop	24	ii	ii	<u>X</u>	<u>X</u>	<u>X</u>	ii	ii	ii	ii	ii
823	9 Bit D Type Flip/Flop	24	ii	ii	ii	<u>X</u>	<u>X</u>	ii	ii	ii	ii	ii
825	8 Bit D Type Flip/Flop	24	ii	ii	ii	<u>X</u>	<u>X</u>	ii	ii	ii	ii	ii

827	10 Bit Buffer/Line Driver	24	ii	ii	ii	ii	X	ii	ii	ii	ii	ii
832	Hex 2-input OR drivers	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
841	10 Bit Transparent Latch	24	ii	ii	ii	X	ii	ii	X	ii	ii	ii
843	9 Bit Transparent Latch	24	ii	ii	ii	X	ii	ii	X	ii	ii	ii
857	Hex 2-line to 1-line multiplexer, 3-state	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
867	Synchronous 8-bit up/down Binary Counter	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
869	Synchronous 8-bit up/down Binary Counter	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
870	Dual 16 by 4 register files	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
873	Dual 4-bit latches, 3-state	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
874	Dual 4-bit D-type flip flops, 3-state	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
876	Dual 4-bit D-type flip flops, 3-state	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
899	9-Bit Latchable Transceiver with parity checker/generator	28	ii	ii	ii	X	X	ii	ii	ii	ii	ii
902	Hex Buffer (TTL Interface)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
906	Open Drain Buffer (Active Pull Down)	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
908	Dual High Voltage CMOS Driver	8	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
912	Display Controller 6 Digit,8 Segment	28	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
914	Hex Schmitt Trigger, External Voltage Inputs	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
922	16 Key Keyboard Encoder	18	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
923	20 Key Keyboard Encoder	20	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
925	4 Digit Counter, Multiplexed 7 Segment Drive	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
990	Octal D-type Latch, 3-state	20	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
992	9-Bit D-type readback latch, 3-state	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
994	10-Bit D-type readback latch	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
996	Octal D-type readback latch	24	ii	ii	ii	ii	ii	ii	X	ii	ii	ii
ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4000	Dual 3 Input NOR Gate plus Inverter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4001	Quad 2 Input NOR Gate	14	ii	ii	ii	ii	ii	X	ii	ii	ii	ii

4002	Dual 4 Input NOR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4006	18 Bit Static Shift Register	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4007	Dual Complementary Pair plus Inverter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4008	4 Bit Full Adder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4009	Hex Inverting Buffers	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4010	Hex Buffer (Non-Inverting)	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4011	Quad 2 Input NAND Gate	14	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
4012	Dual 4 Input NAND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4013	Dual D Flip/Flop with Set/Reset	14	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
4014	8 Bit Static Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4015	Dual 4 Bit Static Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4016	Quad Bi-Lateral Switch	14	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
4017	Decade Counter/Divider	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4018	Presetable Divide-by-N Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4019	Quad AND/OR Select Gate	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4020	14 Stage Ripple Carry Binary Counter	16	X	ii	ii	ii	ii	ii	ii	ii	ii	ii
4021	8 Bit Static Shift Register	16	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
4022	Divide-by-8 Counter/Divider	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4023	Triple 3 Input NAND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4024	7 Bit Binary Counter	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4025	Triple 3 Input NOR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4026	Bcd Decade Counter, 7-Segment Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4027	Dual J-K Flip/Flop	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4028	BCD-to-Decimal Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4029	Presetable Up/Down Binary/Decade Counter	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4030	Quad EX-OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4031	64- Stage Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4032	Triple Positive Logic Serial Adders	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4033	Bcd Decade Counter, 7-Segment Decoder	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4034	8-Bit Bi-Directional Shift Register	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii

4035	4 Bit Shift Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4038	Triple Negative Logic Serial Adders	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4040	12 Bit Binary Ripple Counter/Divider	16	<u>X</u>	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4041	Quad True/Complement Buffer	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4042	Quad D Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4043	Quad Tri-State NOR R/S Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4044	Quad Tri-State NAND R/S Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4045	21-Stage Binary Counter/Divide W/ Oscillator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4046	Phase Locked Loop	16	<u>X</u>	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4047	Monostable/Astable Multivibrator	14	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4048	8-Input Expandable Multifuntion Gate	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4049	Hex Inverting Buffer	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4050	Hex Buffer	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4051	Single 8 Channel Multiplexer	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4052	Differential 4 Channel Multiplexer	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4053	Triple 2 Channel Multiplexer	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4054	BCD to 7 Segment LCD Decoder/ Drive	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4055	Bcd To 7-Segment Decoder Lcd	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4056	Bcd To 7-Segment Decoder Lcd Driver	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4059	Programmable Divide-by-N Counter	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4060	14 Stage Ripple Carry Binary Counter	16	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4063	4 Bit Magnitude Comparator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4066	Quad Bi-Lateral Switch	14	<u>X</u>	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4067	16 Channel Analogue Multiplexer/ Demultiplexer	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4068	8 Input NAND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4069	Hex Inverter	14	ii	ii	ii	ii	ii	<u>X</u>	ii	ii	ii	ii	ii
4070	Quad Exclusive OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4071	Quad 2 Input OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii

4072	Dual 4 Input OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4073	Triple 3 Input AND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4075	Triple 3 Input OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4076	Tri-State Quad Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4077	Quad Exclusive NOR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4078	8 Input NOR/OR Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4081	Quad 2 Input AND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4082	Dual 4 Input AND Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4085	Dual 2 Wide Input AND/OR Invert Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4086	Expandable 4 Wide,2 Input AND-OR-INVERT Gate	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4089	Cascadable 4-Bit Binary Rate Multiplier	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4093	Quad 2 Input NAND Schmitt Trigger	14	ii	ii	ii	ii	ii	X	ii	ii	ii	ii
4094	8 Stage Shift and Store Bus Register	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4095	Gated J-K Master Slave Flip-Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4096	Gated J-K Master Slave Flip-Flop	14	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4097	Differential 8-Channel Mux/Demux	24	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4098	Retriggerable Dual Monostable Multivibrator	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
4099	8 Bit Addressable Latch	16	ii	ii	ii	ii	ii	ii	ii	ii	ii	ii
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